

Hspice User Guide

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Hspice User Guide

HSPICE® User Guide: Simulation and Analysis Version B-2008.09, September 2008

HSPICE User Guide: Simulation and Analysis

HSPICE® User Guide: Basic Simulation and Analysis Version J-2014.09, September 2014

HSPICE User Guide: Basic Simulation and Analysis

HSPICE® Simulation and Analysis User Guide Version X-2005.09, September 2005

HSPICE Simulation and Analysis User Guide

HSPICE® Signal Integrity User Guide xi A-2007.12 About This Manual This manual describes how to use HSPICE to

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maintain signal integrity in your chip design. Inside This Manual This manual contains the chapters described below. For descriptions of the other manuals in the HSPICE documentation set, see the next section, The HSPICE Documentation Set.

HSPICE User Guide: Signal Integrity

iii Contents Inside This Manual.
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. xiii The HSPICE Documentation Set
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HSPICE User Guide: Signal Integrity

The first statement in an HSPICE file must be a title statement and the last must be the end statement (.END). The other Statements may be in any order.

4.3 NETLIST (CIRCUIT DESCRIPTION) The elements in the circuit to be simulated are defined to HSPICE by numbering (or naming) all of the nodes (junctions of components).

HSPICE User Guide V1.1

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i Comments? E-mail your comments about Synopsys documentation to doc@synopsys.com HSPICE Simulation and Analysis User Guide Release U-2003.03-PA, March 2003

HSPICE Simulation and Analysis User Guide - dartec.com

HSPICE® Signal Integrity User Guide xi X-2005.09 About This Manual This manual describes how to use HSPICE to maintain signal integrity in your chip design. Inside This Manual This manual contains the chapters described below. For descriptions of the other manuals in the HSPICE documentation set, see the next section, “The HSPICE Documentation Set.”

HSPICE Signal Integrity User Guide - University of Rochester

HSPICE® User Guide: RF Analysis xi E-2010.12 About This Guide This manual contains detailed reference information, application examples, and design flow descriptions that show how HSPICE RF

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features can be used for RF circuit characterization. The manual supplements the HSPICE user

HSPICE User Guide, RF Analysis

HSPICE® Reference Manual: Commands and Control Options Version B-2008.09, September 2008

HSPICE Reference Manual: Commands and Control Options

HSPICE User Guide: Transmission Lines Supplement. INTRODUCTION. This document should be read in conjunction with the 'HSPICE User Guide'. It covers additional information required to model and simulate transmission lines with HSPICE. A transmission line is a device intended to deliver an output signal at a distance from the point of signal input.

HSPICE User Guide: Transmission Lines Supplement

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HSPICE® and RF Command Reference - Rudrajit

Home | Computer Science

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Star-Hspice User Guide, Release 2001.4

iii Using This Manual This manual describes the Star-Hspice circuit and device simulation software and how to use it. Audience This manual is intended for design engineers who use Star-Hspice to develop, test, analyze, and modify circuit designs. How this Manual is Organized

Star-Hspice User Guide - NCU

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HSPICE - Synopsys

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HSPICE Precision Parallel (HPP) technology HPP is a new multi-threading technology that delivers up to 7X simulation speed-up for analog and mixed-signal designs. The technology scales effectively up to 8 cores and can handle post-layout circuits larger than 10 million elements. To learn more about HPP technology, please download the white paper.

What's New in HSPICE - Synopsys

This redirection can either be automatic (all newly plugged devices are redirected), or manual (the user selects which devices (s)he wants to redirect). For redirection to work, the virtual machine must have an USB2 EHCI controller (this implies 3 additional UHCI controllers). It also needs to have Spice channels for USB redirection.

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Spice User Manual

LTspice Manual and Guidelines.

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