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Coarse-grained reconfigurable
architectures (CGRAs) offer performance

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and energy efficiency close to those of ASICs and a programmability level closer to application software, offering a good trade-off between NRE and device cost. However, CGRAs have not yet been widely adopted because of a lack of mature architectures and programming tools.

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Abstract: Reconfigurable architectures can bring unique capabilities to computational tasks. They offer the performance and energy efficiency of hardware with the flexibility of software. In some domains, they are the only way to achieve the required, real-time

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performance without fabricating custom integrated circuits.

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The result is a valuable book that is a unique record of the state of the art in research into field programmable logic and reconfigurable computing. The

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contributions are organized into twenty-four chapters and are grouped into three main categories: architectures, tools and applications.

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applications, for CAD tool development,
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Torc: Tools for Open Reconfigurable Computing

Applied Reconfigurable Computing.

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Architectures, Tools, and Applications:
14th International Symposium, ARC
2018, Santorini, Greece, May 2-4, 2018,
... (Lecture Notes in Computer Science
(10824)) 1st ed. 2018 Edition

Applied Reconfigurable Computing. Architectures, Tools ...

Reconfigurable computing is a computer

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architecture combining some of the flexibility of software with the high performance of hardware by processing with very flexible high speed computing fabrics like field-programmable gate arrays. The principal difference when compared to using ordinary microprocessors is the ability to make substantial changes to the datapath

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itself in addition to the control flow. On the other hand, the main difference from custom hardware, i.e. application-specific integr

Reconfigurable computing - Wikipedia

Although reconfigurable computing promises huge performance benefits for

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a wide range of different applications, there are many factors that have limited its effectiveness so far. Perhaps most concerning, current generation commercial FPGA architectures and design tools simply do not provide adequate support for these types of applications. In this talk we will discuss the details [...]

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Candidate Talk: Reconfigurable Computing: Architectural ...

Generally, tools for configurable computing systems can be split up in two parts, CAD tools for reconfigurable array and compilation tools for CPU. The front-end compiler is an integrated tool, and will generate a structural hardware

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representation that is input of hardware design flow.

Reconfigurable computing

Reconfigurable computing (RC) devices or units are systems or architectures (Hardware HW or Software SW) that are able to adapt to the application or environmental changes on the fly.

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Introduction to Reconfigurable Computing: Architectures ...

To achieve this goal, researchers will investigate reconfigurable computing architectures and software environments that can deliver specialized, data-intensive application performance without sacrificing

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versatility or programmability, and without the need to develop specialized circuits for each application.

DARPA Selects Teams to Unleash Power of Specialized ...

In conclusion, EXTRA focuses on the fundamental building blocks for run-time reconfigurable exascale HPC systems:

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new reconfigurable architectures with very low reconfiguration overhead, new tools that truly take reconfiguration as a design concept, and applications that are tuned to maximally exploit run-time reconfiguration techniques.

Exploiting eXascale Technology with Reconfigurable ...

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In this survey we explore the hardware aspects of reconfigurable computing machines, from single chip architectures to multi-chip systems, including internal structures and external coupling. We...

Reconfigurable Computing: A Survey of Systems and Software ...

Reconfigurable computers serve as

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affordable, fast, and accurate tools for developing designs ranging from single chip architectures to multi-chip and embedded systems. Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable

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computing.

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Placing a bet on the red-hot low-code/no-code market, Google LLC today debuted its new Business Application Platform category of software encompassing application programming interface

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